

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Application Number	Unassigned
Filing Date	July 25, 2001
First Named Inventor	Takahiro Ohnakado
Group Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	401308

1050 U.S. 570

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[illegible][illegible]

OTHER - NON PATENT LITERATURE DOCUMENTS				
Examiner Initials	Doc. No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number (s), publisher, city and/or country where published.	Translation	
			Yes	No**
SW	A 3	Dutoit et al., "Lateral Polysilicon p-n Diodes", Journal of Electrochemical Society, Volume 125, Number 10, 1978, pages 1648-1651	X	
SW	A 4	Miyawaki et al., "A 20-mm ² , 1.8-V-Only, 16-Mb DINOR Flash Memory With Gate-Protected-Poly-Diode (GPPD) Charge Pump", IEEE Journal of Solid-State Circuits, Volume 34, 1999, pages 1551-1556	X	
SW	A 5	Ker et al., "Novel Octagonal Device Structure For Output Transistors In Deep-Submicron Low-Voltage CMOS Technology", IEDM, 1996, pages 889-892	X	

Date Considered

1/12/05

- * A concise statement of relevance is being submitted in lieu of a translation. 37 CFR 1.98(a)(3).
- + An English-language equivalent/patent, or an English-language abstract, or an English-language version of the search report or action by a foreign patent office in a counterpart foreign application indicating the degree of relevance found by the foreign office is being submitted in lieu of a concise explanation of relevance under 37 CFR 1.98(a)(3).